	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	24	(patrick near2 lin).in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 16:42
2	BRS	L3	3	1 not 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 16:42
3	BRS	L2	21	1 and taipei	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 16:44
4	BRS	L4	16	chipset same (south near3 bridge) same (bmc or ((baseboard or management) near3 controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 17:02
5	BRS	L5	12461	(bmc or ((baseboard or management) near3 controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 17:02
6	BRS	L6	6	(south near3 bridge) same (bmc or (baseboard with controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 17:20
7	BRS	L7	5	6 not 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 17:03
8	BRS	L8	4	(south near3 bridge) and (bmc or (baseboard with controller)) and (sio or (super with "i/o"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 18:16
9	BRS	L9	6	(chipset) same (bmc or (baseboard with controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 17:20
10	BRS	L10	2	(chipset) and (bmc or (baseboard with controller)) and (sio or (super with "i/o"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 17:56
11	BRS	L11	8	(south near3 bridge) and (bmc or (baseboard with controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 17:29
12	BRS	L12	34	(chipset) and (bmc or (baseboard with controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 18:00

10/678,554

13	BRS	L13	11	(6 or 8 or 9 or 10)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 17:30

	Type	L #	Hits	Search Text	DBs	Time Stamp
14	BRS	L14	2	11 not 13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 17:34
15	BRS	L15	28	12 not (13 or 14)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 17:34
16	BRS	L16	13	(bmc or (baseboard with controller)) same (sio or (super with "i/o"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 17:57
17	BRS	L17	9	16 not (13 or 14 or 15)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 18:01
18	BRS	L18	174	(system with (chip or ic or (integrated near3 circuit))) and (bmc or (baseboard with controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 18:01
19	BRS	L19	24	(system with (chip or ic or (integrated near3 circuit))) same (bmc or (baseboard with controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 18:01
20	BRS	L20	19	19 not (13 or 14 or 15 or 16)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 18:09
21	BRS	L21	136	18 not (13 or 14 or 15 or 16 or 19)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 18:15
22	IS&R	L22	8613	(710/1,2,8,14,15,18,62-64,72-74,100,300,104,305,306,311,312).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 18:16
23	BRS	L23	4	21 and 22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 18:21
24	BRS	L24	132	21 not 23	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/07/05 18:21